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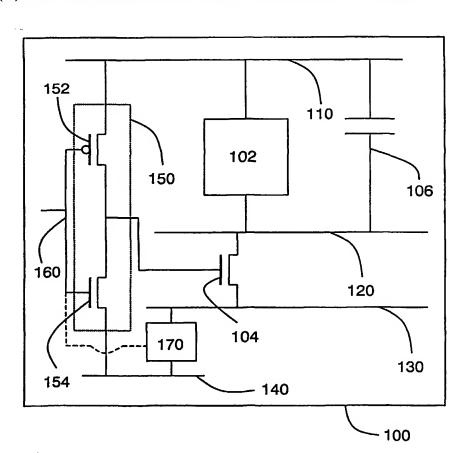
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[Continued on next page]

(54) Title: INTERGRATED CIRCUIT AND BATTERY POWERED ELECTRONIC DEVICE



An integrated (57) Abstract: circuit (100) has a circuit portion (102) that can be switched to a standby mode through an enable transistor (104), which is coupled between an internal power supply line (120) and an external power supply line (130). The enable transistor (104) is controlled by control circuitry via a control line (160). The control line (160) is coupled to the gates of a first transistor (152) and a further transistor (154) of a logic gate (150). The substrate of the further transistor (154) is coupled to a backbias generator (170). Consequently, when the enable transistor (104) is switched off, the further transistor (154) is enabled and applies a substantial backbias to the gate of the enable transistor (104), thus dramatically reducing the leakage current from the circuit portion (102) through the enable transistor (104).

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INTERNATIONAL SEARCH REPORT

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A. CLASSIFICATION OF SUBJECT MATTER IPC 7 H03K19/00									
According to	o International Patent Classification (IPC) or to both national classifica	ilon and IPC							
B. FIELDS SEARCHED									
Minimum do IPC 7	ocumentation searched (classification system followed by classification H03K	on symbols)							
Documenta	tion searched other than minimum documentation to the extent that se	uch documents are included in the fields se	earched						
Electronic data base consulted during the international search (name of data base and, where practical, search terms used)									
EPO-In	ternal, WPI Data, PAJ								
C. DOCUM	ENTS CONSIDERED TO BE RELEVANT								
Category °	Citation of document, with indication, where appropriate, of the rela	evant passages	Relevant to claim No.						
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A	EP 0 690 510 A (NIPPON TELEGRAPH TELEPHONE) 3 January 1996 (1996-0 page 6, line 40 -page 7, line 5; 8-9B	1-3							
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Further documents are listed in the continuation of box C. X Patent family members are listed in annex.									
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